Introduction

The PCM-3810A Solid-State Disk Module uses Flash, EPROM, or SRAM chips inserted into its standard 32-pin sockets to emulate one or two floppy disk drives. The PCM-3810A also supports one DOC® 2000 chip to emulate one HDD. The PCM-3810A’s two banks of chips provide from 360 KB to 1.44 MB of disk storage space each, depending on the size and number of memory devices.

The PCM-3810A’s banks are assigned drive labels (1st, 2nd, 3rd or 4th), or they can be individually disabled. One PCM-3810A drive can be set as the boot drive and copying the necessary boot files to it. The drive designation is purely internal to the module, so you do not need to make any changes to the BIOS setup. DOS will determine the final drive designation.

Specifications

- **Board size:** 96 x 90 mm (3.7" x 3.5")
- **Power:** +5 V @ 1 A maximum for normal applications
- **Batteries:** Two 3 V (190 mAHr) lithium back-up batteries
- **Operating temperature:** 0 ~ 60° C (32 ~ 140° F)

Features

- Emulates up to two floppy disk drives (1st, 2nd, 3rd or 4th) under DOS environment
- Disk sizes: 360 KB to 2.88 MB (2 banks linked together)
- Supports one DOC 2000 chip to emulate one HDD
- Drive designation: DOS drive
- 6 individual 32-pin memory sockets, divided into two banks: one bank for each emulated drive.
- Up to two PCM-3810A’s can be installed in one PC
- Power-on auto-boot feature
- On-board Flash programming circuitry with easy-to-use menu driven programming utility software
- 2 Lithium back-up batteries (3 V, 190 mAHr) for 3 years of data retention (depending on memory configuration)
- On-board LED signals low battery voltage (< 2.5 V)
- Connector for external battery
- Battery status displayed when booting
- Each card occupies only 16 KB of system memory space

Component Layout

![Component Layout Diagram]


Initial Inspection
In addition to this user’s manual, your shipping box should contain the following items:

- PCM-3810A Dual Flash/RAM/ROM disk card
- A software utility diskette

**WARNING!** Discharge your body’s static electric charge by touching the back of the grounded chassis of the system unit (metal) before handling the board. You should avoid contact with materials that hold a static charge such as plastic, vinyl, and styrofoam. The board should be handled only by its edges to avoid static damage to its integrated circuits. Avoid touching the exposed circuit connectors.

Setting Jumpers
You configure the board to match the needs of your application by setting jumpers. A jumper is the simplest kind of electric switch. It consists of two metal pins and a small metal clip (often protected by a plastic cover) that slides over the pins to connect them. To “close” a jumper you connect the pins with the clip. To “open” a jumper you remove the clip. Sometimes a jumper will have three pins, labeled 1, 2, and 3. In this case you would connect either pins 1 and 2 or 2 and 3.

- Open/Off
- Closed/On
- Closed 2-3

The jumper settings are schematically depicted in this manual as follows:

- Open/Off
- Closed/On
- Closed 2-3

A pair of needle-nose pliers may be helpful when working with jumpers.

If you have any doubts about the best hardware configuration for your application, contact your local distributor or sales representative before you make any changes.

Generally, you simply need a standard cable to make most connections.

**External Battery Connector (J8)**
You can use an internal/external battery to provide the power for the PCM-3810A by setting J8.

<table>
<thead>
<tr>
<th>Description</th>
<th>J8</th>
</tr>
</thead>
<tbody>
<tr>
<td>Using Internal Battery Power</td>
<td>4 3 2 1</td>
</tr>
<tr>
<td>External Battery Connector</td>
<td>4 3 2 1</td>
</tr>
</tbody>
</table>

Battery Enable/Disable (J5, J6, J7)
The PCM-3810A’s 3 V, 190 mAHr Lithium batteries supply power to SRAM chips to enable data retention when you turn off your system’s power. You can enable or disable the backup supply for each bank independently by setting J5, J6, and J7 as shown in the following table:

<table>
<thead>
<tr>
<th>Battery Backup</th>
<th>Enabled</th>
<th>Disabled</th>
</tr>
</thead>
<tbody>
<tr>
<td>Bank 1</td>
<td>J6</td>
<td>J6</td>
</tr>
<tr>
<td>Socket 1 - 3</td>
<td>1 2 3</td>
<td>1 2 3</td>
</tr>
<tr>
<td>Bank 2</td>
<td>J5</td>
<td>J5</td>
</tr>
<tr>
<td>Socket 3</td>
<td>1 2 3</td>
<td>1 2 3</td>
</tr>
<tr>
<td>Bank 2</td>
<td>J7</td>
<td>J7</td>
</tr>
<tr>
<td>Socket 1, 2</td>
<td>1 2 3</td>
<td>1 2 3</td>
</tr>
</tbody>
</table>

**Note:**
1. If you install SRAM chips on the PCM-3810A, always set the “Battery backup” as “Enabled” to prevent battery power leakage.
2. If you install a DOC® 2000 chip on socket 3, bank 2, always set J5 as “disabled” to prevent battery power leakage in bank 2.

**Memory Address Setup (J1, J2, J3)**
You can set the PCM-3810A’s memory address to ensure compatibility with other boards installed in the system.

<table>
<thead>
<tr>
<th>Memory Address</th>
<th>J1</th>
<th>J2</th>
<th>J3</th>
</tr>
</thead>
<tbody>
<tr>
<td>Disable</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>C800</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>CC00</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>D000</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>D400</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>D800</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>DC00</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>E000</td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
Make sure that you are properly grounded before handling memory devices or the board. You can discharge any static electricity you may have built up by touching the case (metal) of your computer before you begin installation. You should also avoid contact with materials that hold a static charge such as plastic, vinyl, and styrofoam. Avoid touching the exposed circuit connectors.

- Treat connectors gently
  The legs of integrated circuits are delicate, and may bend before reaching the bases of sockets. Inspect each leg before you begin, test-fit it gently into the socket, and straighten any bent legs before you insert.

- Install memory chips with the correct orientation

<table>
<thead>
<tr>
<th>Chip Enabled (J4)</th>
</tr>
</thead>
<tbody>
<tr>
<td>This setting allows you to enable the chip installed on socket 3, bank 2.</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Chip Enabled</th>
<th>J4</th>
</tr>
</thead>
<tbody>
<tr>
<td>DOC® 2000</td>
<td>![Chip Enabled (J4) Diagram]</td>
</tr>
<tr>
<td>EPROM/SRAM/Flash</td>
<td>![Chip Enabled (J4) Diagram]</td>
</tr>
</tbody>
</table>

## Memory Device Selection

### Memory Devices and Disk Sizes

The table below gives the number of chips required to emulate common floppy disk sizes. The Flash memory chips listed in the table below may be programmed on board.

<table>
<thead>
<tr>
<th>Device Code</th>
<th>Manufacturer</th>
<th>Single-chip size (KB)</th>
<th>360 KB</th>
<th>720 KB</th>
<th>1.44 MB</th>
<th>2.88 MB</th>
</tr>
</thead>
<tbody>
<tr>
<td>27C010</td>
<td>ATMEL and AMD</td>
<td>128</td>
<td>x 3</td>
<td>x 6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>27C040</td>
<td>ATMEL and AMD</td>
<td>512</td>
<td>x 1</td>
<td>x 2</td>
<td>x 3</td>
<td>x 6</td>
</tr>
<tr>
<td>581000P</td>
<td>Sony</td>
<td>128</td>
<td>x 3</td>
<td>x 6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>584000P</td>
<td>Sony</td>
<td>512</td>
<td>x 1</td>
<td>x 2</td>
<td>x 3</td>
<td>x 6</td>
</tr>
<tr>
<td>29C010 Flash (+5 V)</td>
<td>ATMEL</td>
<td>128</td>
<td>x 3</td>
<td>x 6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>28F010 Flash (+12 V)</td>
<td>Intel/AMD</td>
<td>128</td>
<td>x 3</td>
<td>x 6</td>
<td>-</td>
<td>-</td>
</tr>
<tr>
<td>29C040</td>
<td>ATMEL</td>
<td>512</td>
<td>x 1</td>
<td>x 2</td>
<td>x 3</td>
<td>x 6</td>
</tr>
</tbody>
</table>

Memory chips must be installed the correct way around. If this is not done the chip will be damaged. Each chip and its corresponding socket will have a small indent (or spot) at it's top end. These must be aligned when the chip is inserted into the socket.

- Don't mix chips of different capacities
  You should only install one type of chip (Flash, RAM, or ROM) in a single bank, and you should also install only chips of the same size.

Note: For bank 2, you can install one DOC 2000 chip on socket 3, and two EPROM/SRAM/Flash chips of the same type and size on sockets 1 and 2. Refer to page 5 for the "IC type" and "DOC 2000 type" options for software configuration.

### Static Random Access Memory (SRAM)

Memory chips are either volatile or nonvolatile. Volatile memories require a constant application of current in order to retain memory, provided by the PC or a backup battery, while nonvolatile memories require current only for write operations. SRAMs are volatile and do not require any external devices for programming or erasing. Instead, they can be formatted, read, and written to as if they were DOS-compatible mechanical drives.

### Erasable Programmable Read-Only Memory (EPROM)

Unlike SRAMs, EPROM devices are nonvolatile. They do not require battery backups, and a power loss will never harm the data they store. They are the least expensive of the three types of memory your PCM-3810A will accept. However, they require use of the utility program for writing and formatting. In addition, whereas flash memories may be erased while installed in the PCM-3810A, EPROMs must be removed for use with an external UV EPROM eraser.

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**Warning!** Ground yourself when handling memory devices and insert them with as little force as possible!
Flash Memory

Flash memory, the newest memory type and was developed to eliminate the disadvantages of both SRAMs and EPROMs, and offer greater flexibility than either. They are nonvolatile, requiring no battery backup, and in some cases can be erased and reprogrammed while on the PCM-3810A board.

The PCM-3810A supports two different kinds of Flash memories, the ATMEAL 29C010, 29C040 (both +5 V) memory, and the AMD/INTEL 28F010 (+12 V) memory. Advantages and disadvantages of each are summarized below.

ATMEAL 29C010/29C040 (+5 V)
Advantages: Can be read, written to, and formatted with normal DOS commands while still within their sockets. Offers greater reliability than SRAMs
Disadvantages: Slower to format and write data to than SRAMs. More expensive than SRAMs

AMD/INTEL 28F010 (+12 V)
Advantages: Does not need to be removed for erasing or reprogramming. Low cost
Disadvantages: Can only be erased and reprogrammed with the PCM-3810A utility program

Software Configuration

This section details how to configure the card to match your application requirements.

Configuration

To configure the PCM-3810A with software, install the PCM-3810A into your computer system, turn on the computer, and boot up the computer. While your computer is booting up, press the “Alt” and “S” keys simultaneously. The main program screen will then appear with the following options:

The Card Locate” option

If you install more than one PCM-3810A in your system, and each PCM-3810A has its own memory address, the “card locate” function allows you to modify each PCM-3810A as follows:
1. Move the cursor (by pressing the “↑” or “↓” key) to “CARD LOCATE” and press “Enter”.
2. Choose the memory address of the PCM-3810A card which you are going to modify.
3. Press “Enter”.
You have now chosen the card you are going to modify. Move the cursor to the next option which you want to modify.

The “Bank Link” option

Each bank of the PCM-3810A emulates one FDD (up to 1.44 MB). The “bank link” function allows you to link the PCM-3810A’s two banks together to form a single bank with larger memory capacity (2.88 MB). The “bank link” function also allows you to set the PCM-3810A as a memory card for your system. (But this does not emulate a FDD.)
1. Move the cursor (by pressing the “↑” or “↓” key) to “Bank Link” and press “Enter”.
2. If you want to link the two banks of PCM-3810A as one bank, choose “LINK ON”. If you want to separate the PCM-3810A’s two banks, choose “LINK OFF”.
3. If you want to set the PCM-3810A as a memory card for your system (but not emulate a FDD), choose “SSD OFF”.

You have finished the “bank link” function setting. Move the cursor to the next option which you want to modify.

The “Bank No.” option

The “Bank No.” option allows you to choose the bank which you are going to modify.
1. Move the cursor (by pressing the "↑" or "↓" key) to "Bank No" and press "Enter".

2. Choose the bank which you are going to modify and press "Enter".

You have finished the "Bank No." function setting. Move the cursor to the next option which you want to modify.

### The "IC Type" option

The "IC Type" option allows you to set the type of ICs which you are going to install or have installed on the bank you are modifying. If you want to install a DOC 2000 on the PCM-3810A, refer to the "DOC 2000" option.

1. Move the cursor (by pressing the "↑" or "↓" key) to "IC Type" and press "Enter".

2. Choose the type of IC (by pressing the "↑" or "↓" key) that you are going to install or have installed on the PCM-3810A. Then press "Enter".

3. If you choose "Bank OFF" and press "Enter", the bank you are modifying will be turned off, and you will be unable to make any further modifications. To turn on the bank again, choose one type of IC and press "Enter".

You have now finished the "IC Type" function setting. Move the cursor to the next option which you want to modify.

### The "Drive No." option

You can set the bank you are modifying as the 1st, 2nd, 3rd or 4th FDD for your system.

1. Move the cursor (by pressing the "↑" or "↓" key) to "Drive No" and press "Enter".

2. Choose 1st (drive A:), 2nd (drive B:), 3rd (drive C:) or 4th (drive D:) for the bank you are modifying.

3. Press "Enter".

You have now finished the "Drive No." function setting. Move the cursor to the next option which you want to modify.

### The "Protect" option

When you use the bank you are modifying to emulate a FDD, you can set the bank to "write enable" or "read only".

1. Move the cursor (by pressing the "↑" or "↓" key) to "Protect" and press "Enter".

2. If you want the bank you are modifying to be writable, choose "Unprotect". If you want the bank you are modifying to be read only, choose "Protect".

3. Press "Enter".

You have now finished the "Protect" function setting. Move the cursor to the next option which you want to modify.

### The "DOC® 2000" option

This function allows you to install a DOC 2000 chip on the 3rd socket of bank 2 to emulate one HDD.

1. Move the cursor (by pressing the "↑" or "↓" key) to "DOC 2000" and press "Enter".

2. If you want to install a DOC 2000 chip on the 3rd socket of bank 2, choose "ON". Otherwise, choose "OFF".
3. Press "Enter".

You have now finished the "DOC 2000" function setting (refer to the DOC 2000 User Manual for more detailed information). Move the cursor to the next option which you want to modify.

**The "Quit Mode" option**

After you have finished all of you modifications, move the cursor to "QUIT MODE" and press "Enter". If you want to save the modifications you have just made, choose "Save & Exit". If you want to abandon the modifications you have just made, choose "Exit".

You have now finished the PCM-3810A's firmware modifications.

**Utility Software**

The PCM-3810's utility software provides BIOS upgrade, BIOS backup, memory chips copy, and a memory chips test for the PCM-3810A.

**Cardprog**

This software provides BIOS backup and BIOS upgrade functions for the PCM-3810A.

DOS command:


Switch options:

- **S** - save BIOS image into a file (i.e. BIOS backup)
- **W** - upgrade BIOS

Example 1 - BIOS backup:

C:\> cardprog /win:C800 /S:3810A.bin

Copy the BIOS of PCM-3810A (located in the C800 segment) to the file 3810.bin located in drive C:.

Example 2 - upgrade BIOS:

C:\> cardprog /win:C800 /W:3810Av30.bin

Upgrade the BIOS of PCM-3810A (located in the C800 segment) from the file 3810Av30.bin located in drive C:.

**Cardfile**

This software provides memory chips copy and test for the PCM-3810A.

DOS command:


Switch options:

- **S** - copy data from chips to files
- **W** - write data from files to chips
- **T** - test memory chips

Device options:

- **erm128** - EPROM 128K
- **srm128** - SRAM 128K
- **flh128** - Flash 128K
- **erm512** - EPROM 512K
- **srm512** - SRAM 512K
- **flh512** - Flash 512K

Begin chip/end chip options:

1 - the first chip (located on socket 1, bank 1)
2 - the second chip (located on socket 2, bank 1)
3 - the third chip (located on socket 3, bank 1)
4 - the fourth chip (located on socket 1, bank 2)
5 - the fifth chip (located on socket 2, bank 2)
6 - the sixth chip (located on socket 3, bank 2)

Example 3 - copy data from chips to files:

C:\> cardfile /win:C800 /S:rom /d:srm128 /n:3-6

Copy the data in the PCM-3810A's SRAM chips 3, 4, 5, and 6 (located in segment C800) to files rom.3, rom.4, rom.5 and rom.6, respectively. Press "Enter" and the following message will appear on the screen:

---

CardFile Version 1.0 for Solid State Disk

"Found the SSD Card in C800....."

write chip 3 to rom.3
write chip 4 to rom.4
write chip 5 to rom.5
write chip 6 to rom.6

---

Example 4 - write data from files to chips:

C:\> cardfile /win:C800 /w:rom /d:srm128 /n:3-6

Copy the data from rom.3, rom.4, rom.5, and rom.6 to the PCM-3810A's SRAM chips 3, 4, 5, and 6 respectively (located in segment C800). Press "Enter" and the following message will appear on the screen:
Example 5 - test memory chips:

Note: Before you execute the test command, copy the data in the chips being tested to other chips or files first. The data in the chips being tested will be overwritten by the test data once you execute the test command.

C:\> cardfile /win:C800 /t /d:flh128 /n:2-5

Test the PCM-3810A’s Flash chips 2, 3, 4 and 5 (located in segment C800). Press “Enter” and the following message will appear on the screen:

Pin Assignments
All PC/104 modules follow the same pin assignments as shown in the table below:

<table>
<thead>
<tr>
<th>Number</th>
<th>Row A</th>
<th>Row B</th>
<th>Row C</th>
<th>Row D</th>
</tr>
</thead>
<tbody>
<tr>
<td>0</td>
<td>--</td>
<td>0 V</td>
<td>0 V</td>
<td>0 V</td>
</tr>
<tr>
<td>1</td>
<td>ICCHCHK</td>
<td>0 V</td>
<td>SBHE</td>
<td>MEMCS16</td>
</tr>
<tr>
<td>2</td>
<td>SD7</td>
<td>RESETDRV</td>
<td>LA23</td>
<td>IOCS16</td>
</tr>
<tr>
<td>3</td>
<td>SD6</td>
<td>+5 V</td>
<td>LA22</td>
<td>IRQ10</td>
</tr>
<tr>
<td>4</td>
<td>SD5</td>
<td>IRQ9</td>
<td>LA21</td>
<td>IRQ11</td>
</tr>
<tr>
<td>5</td>
<td>SD4</td>
<td>-5 V</td>
<td>LA20</td>
<td>IRQ12</td>
</tr>
<tr>
<td>6</td>
<td>SD3</td>
<td>DRQ2</td>
<td>LA19</td>
<td>IRQ15</td>
</tr>
<tr>
<td>7</td>
<td>SD2</td>
<td>-12 V</td>
<td>LA18</td>
<td>IRQ14</td>
</tr>
<tr>
<td>8</td>
<td>SD1</td>
<td>ENDFR</td>
<td>LA17</td>
<td>DACK0</td>
</tr>
<tr>
<td>9</td>
<td>SD0</td>
<td>+12 V</td>
<td>MEMR</td>
<td>DACK0</td>
</tr>
<tr>
<td>10</td>
<td>ICCHRDY</td>
<td>(KEY)</td>
<td>MBMW</td>
<td>DACK5</td>
</tr>
<tr>
<td>11</td>
<td>AEN</td>
<td>SMEMW</td>
<td>SD8</td>
<td>DRO5</td>
</tr>
<tr>
<td>12</td>
<td>SA19</td>
<td>SMEMR</td>
<td>SD9</td>
<td>DACK6</td>
</tr>
<tr>
<td>13</td>
<td>SA18</td>
<td>IOW</td>
<td>SD10</td>
<td>DRO6</td>
</tr>
<tr>
<td>14</td>
<td>SA17</td>
<td>ICR</td>
<td>SD11</td>
<td>DACK7</td>
</tr>
<tr>
<td>15</td>
<td>SA16</td>
<td>DACK3</td>
<td>SD12</td>
<td>DRO7</td>
</tr>
<tr>
<td>16</td>
<td>SA15</td>
<td>DRO8</td>
<td>SD13</td>
<td>+5 V</td>
</tr>
<tr>
<td>17</td>
<td>SA14</td>
<td>DACK1</td>
<td>SD14</td>
<td>MASTER</td>
</tr>
<tr>
<td>18</td>
<td>SA13</td>
<td>DRO1</td>
<td>SD15</td>
<td>0 V</td>
</tr>
<tr>
<td>19</td>
<td>SA12</td>
<td>REFRESH</td>
<td>(KEY)</td>
<td>0 V</td>
</tr>
<tr>
<td>20</td>
<td>SA11</td>
<td>SYSCLK</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>21</td>
<td>SA10</td>
<td>IRQ7</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>22</td>
<td>SA9</td>
<td>IRQ6</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>23</td>
<td>SA8</td>
<td>IRQ5</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>24</td>
<td>SA7</td>
<td>IRQ4</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>25</td>
<td>SA6</td>
<td>IRQ3</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>26</td>
<td>SA5</td>
<td>DACK2</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>27</td>
<td>SA4</td>
<td>TC</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>28</td>
<td>SA3</td>
<td>BALE</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>29</td>
<td>SA2</td>
<td>+5 V</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>30</td>
<td>SA1</td>
<td>OSC</td>
<td>--</td>
<td>--</td>
</tr>
<tr>
<td>31</td>
<td>SA0</td>
<td>0 V</td>
<td>--</td>
<td>--</td>
</tr>
</tbody>
</table>