NEKBONE
PERFORMANCE
PORTABILITY

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WHAT IS NEK5000?

- Spectral element CDF Solver for:
  - Unsteady incompressible Navier-Stokes
  - Low mach number flows
  - Heat transfer and species transport
  - Incompressible magnetohydrodynamics

- Code:
  - Open source
    - https://github.com/Nek5000/Nek5000
  - Written in Fortran 77 and C
  - MPI for parallelization

- Features:
  - Highly scalable, scales to over a million processes
  - High order spatial discretization using spectral elements
  - High order semi-implicit time stepping
WHAT IS NEKBONE?

- Nekbone is a mini-app derived from Nek5000
  - Originally developed by Katherine Heisey, Paul Fischer
- Nek5000 is complicated to setup, run, and modify.
  - Nekbone is simpler and enables more rapid investigations
- Nekbone represents significant kernels in Nek5000
  - Large percentage of the work in Nek5000
  - Solve Poisson problem similar to pressure solve in Nek5000
  - Represents key kernels and operation mix from Nek5000
    - matrix-matrix multiplication
    - inner products
    - nearest neighbor communication
    - MPI_Allreduce
- Implemented using:
  - Fortran 77, C, MPI, OpenMP, OpenACC
- Used for:
  - DOE machine acquisitions: CORAL systems
  - Exascale co-design activities: DOE FastForward, DesignForward
  - Programming model exploration
- Available at:
  - https://cesar.mcs.anl.gov/content/software/thermal_hydraulics (Original)
  - https://asc.llnl.gov/CORAL-benchmarks/ (CORAL)
  - https://repocafe.cels.anl.gov/repos/nekbone/ (OpenMP)
  - https://github.com/Nek5000/Nekbone/tree/cuda-openacc (OpenACC)
NEKBONE IN A NUTSHELL

Solver (CG)
- solveM
- glsc3
  - AllReduce
- add2s1
- ax
  - ax_e
    - local_grad3
      - (3x) mxm
    - wr-ws-wt
    - local_grad3_t
      - (3x) mxm
      - (2x) add2
- gs_op
- add2s2
- glsc3
  - AllReduce
- add2s2
- add2s2
- glsc3
  - AllReduce

Memory Bandwidth Intensive
Floating Point Intensive
Network Intensive
Memory Operation Intensive
Wrapper routines

- gs_gather
- pw_exec
  - pw_exec_recvs
    - MPI_Irecv
  - gs_scatter
  - pw_exec_sends
    - MPI_Isend
- comm_wait
- gs_gather
- gs_scatter
NEKBONE IN A NUTSHELL (SINGLE NODE)

- solveM
- glsc3
  - AllReduce
- add2s1
- ax
  - ax_e
    - local_grad3
      - (3x) mxm
    - wr-ws-wt
    - local_grad3_t
      - (3x) mxm
      - (2x) add2
- gs_op
- add2s2
- glsc3
  - AllReduce
- add2s2
- add2s2
- glsc3
  - AllReduce

Memory Bandwidth Intensive
Floating Point Intensive
Network-Intensive
Memory Operation Intensive
Wrapper routines
## NEKBONE COMPUTE PERFORMANCE MODEL

<table>
<thead>
<tr>
<th>Function</th>
<th>Routine (major)</th>
<th>Data</th>
<th>Code</th>
<th>Loads</th>
<th>Stores</th>
<th>FPOps</th>
</tr>
</thead>
<tbody>
<tr>
<td>solveM</td>
<td>copy</td>
<td>z,r</td>
<td>(z(i)=r(i))</td>
<td>N</td>
<td>N</td>
<td>0</td>
</tr>
<tr>
<td>glsc3</td>
<td></td>
<td>r,c,z</td>
<td>(t=t+r(i)c(i)z(i))</td>
<td>3N</td>
<td>0</td>
<td>3N</td>
</tr>
<tr>
<td>add2s1</td>
<td></td>
<td>p,z</td>
<td>(p(i)=C^*p(i)z(i))</td>
<td>2N</td>
<td>N</td>
<td>2N</td>
</tr>
<tr>
<td>local_grad3</td>
<td>mxm (3)</td>
<td>p,ur,dxm1</td>
<td></td>
<td>N</td>
<td>0</td>
<td>6N_{x,N}</td>
</tr>
<tr>
<td>wrwswt</td>
<td></td>
<td>g,ur,us,ut</td>
<td>(u_r(i)=g(1,i)u_r(i)+g(2,i)u_s(i)+g(3,i)u_t(i))</td>
<td>6N</td>
<td>0</td>
<td>15N</td>
</tr>
<tr>
<td>local_grad3_t</td>
<td>mxm (3)</td>
<td>w,ur,dxtm1</td>
<td></td>
<td>0</td>
<td>N</td>
<td>(6N_{x}+2)N</td>
</tr>
<tr>
<td>gs_op</td>
<td>Pt-2-pt</td>
<td></td>
<td>Gather Scatter communication</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>add2s2</td>
<td></td>
<td>x,p</td>
<td>(x(i)=x(i)+C^*p(i))</td>
<td>2N</td>
<td>N</td>
<td>2N</td>
</tr>
</tbody>
</table>
## HARDWARE COMPARISON

**INTEL XEON PHI KNIGHTS LANDING (7230) AND NVIDIA TESLA P100-16**

<table>
<thead>
<tr>
<th></th>
<th>KNL</th>
<th>P100 GPU</th>
</tr>
</thead>
<tbody>
<tr>
<td>Cores/SMX</td>
<td>64</td>
<td>56</td>
</tr>
<tr>
<td>SIMD DP Width</td>
<td>8</td>
<td>32</td>
</tr>
<tr>
<td>Clock Speed [MHz]</td>
<td>1100</td>
<td>1329</td>
</tr>
<tr>
<td>Peak f32 [TFlop/s]</td>
<td>4.5</td>
<td>9.3</td>
</tr>
<tr>
<td>Peak f64 [TFlop/s]</td>
<td>2.25</td>
<td>4.7</td>
</tr>
<tr>
<td>Peak DGEMM [Tflops/s]</td>
<td>1.9</td>
<td>4.5</td>
</tr>
<tr>
<td>Memory (IPM) [GB]</td>
<td>16</td>
<td>16</td>
</tr>
<tr>
<td>IPM Bandwidth [GB/s]</td>
<td>~600</td>
<td>732</td>
</tr>
<tr>
<td>STREAM Bandwidth [GB/s]</td>
<td>488</td>
<td>574</td>
</tr>
<tr>
<td>L1 cache [kB]/(Core/SMX)</td>
<td>32(D) +32(I)</td>
<td>64 (L1/Tex.)</td>
</tr>
<tr>
<td>L2 cache [MB]</td>
<td>32</td>
<td>4</td>
</tr>
<tr>
<td>TDP [W]</td>
<td>215</td>
<td>250</td>
</tr>
</tbody>
</table>

- P100 has 2.4x DGEMM rate of KNL
- P100 has 1.2x STREAM rate of KNL
NEKBONE PERFORMANCE ON KNL

Solver time for different problem sizes

Polynomial Order 16

Using single core, time increases linearly with problem size

Using 64 cores, time is roughly flat until problem has enough parallelism to fill all cores

Using 64 cores, time increases linearly with problem size for large enough problems

<table>
<thead>
<tr>
<th>Small MXM Implementation</th>
<th>% of peak DGEMM efficiency</th>
</tr>
</thead>
<tbody>
<tr>
<td>Naive</td>
<td>18</td>
</tr>
<tr>
<td>Unrolled</td>
<td>26</td>
</tr>
<tr>
<td>XSMM</td>
<td>51</td>
</tr>
</tbody>
</table>
### NEKBONE PERFORMANCE ANALYSIS ON KNL

<table>
<thead>
<tr>
<th>Routine</th>
<th>Time (s)</th>
<th>% Solve time</th>
<th>GB/s</th>
<th>GFlop/s</th>
<th>% of peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>solveM</td>
<td>2.90E-2</td>
<td>3.7</td>
<td>462.5</td>
<td>0</td>
<td>95</td>
</tr>
<tr>
<td>glsc3</td>
<td>1.56E-1</td>
<td>20.2</td>
<td>351.2</td>
<td>43.9</td>
<td>72</td>
</tr>
<tr>
<td>add2s1</td>
<td>4.23E-2</td>
<td>5.4</td>
<td>475.6</td>
<td>39.6</td>
<td>97</td>
</tr>
<tr>
<td>local_grad3</td>
<td>8.31E-2</td>
<td>10.7</td>
<td>969.6</td>
<td>51</td>
<td></td>
</tr>
<tr>
<td>wrwswt</td>
<td>1.28E-1</td>
<td>16.5</td>
<td>314.1</td>
<td>98.1</td>
<td>64</td>
</tr>
<tr>
<td>local_grad3_t</td>
<td>1.09E-1</td>
<td>14.1</td>
<td>754.4</td>
<td>40</td>
<td></td>
</tr>
<tr>
<td>gs_op</td>
<td>8.89E-2</td>
<td>11.5</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>add2s2</td>
<td>1.23E-2</td>
<td>16.02</td>
<td>480.3</td>
<td>40.0</td>
<td>98</td>
</tr>
</tbody>
</table>

- Memory bandwidth intensive kernels obtain 64-95% of STREAM
- Compute intensive kernels obtains 40-51% of peak DGEMM rate
- Memory operation intensive kernels are ~10% of runtime, no efficiency model yet
- Network performance is not investigated
OPENACC CODE CHANGES

- **Simple changes:**
  - Added OpenACC Parallel Loop pragma before loop
  - Functions:
    - glsc3, add2s2, add2s1 (single loop bandwidth bound kernels)

- **Moderately complex changes:**
  - Manually inlined functions and merged loop nests
  - Added OpenACC Parallel Loop with Collapse to resulting outer loops
  - Functions:
    - local_grad, wrwswt, local_grad_t

- **Complex changes:**
  - Serial data structure describing gather/scatter operations augmented to enable parallelism
    - Required for any form of shared memory parallelism including OpenMP implementation for CPU
  - Loops restructured to utilize new data structures
  - Added OpenACC loop on outer loop driving gather and scatter
  - Functions:
    - gs_gathe, gs_scatter
NEKBONE PERFORMANCE ON P100 & KNL

Sufficient DOF required to efficiently utilize cores occurs at different problem sizes for CPU and GPU. GPU performance lags CPU for small problem sizes. For largest DOF GPU performance exceeds naïve CPU implementation. Further room to optimized GPU performance with optimized small matrix multiply, may exceed CPU performance.

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**Solver time for different problem sizes**

Polynomial Order 8

<table>
<thead>
<tr>
<th>code</th>
<th>time</th>
</tr>
</thead>
<tbody>
<tr>
<td>KNL-naive-omp64</td>
<td>1.12</td>
</tr>
<tr>
<td>KNL-unrolled-omp64</td>
<td>0.91</td>
</tr>
<tr>
<td>KNL-xsmm-omp64</td>
<td>0.75</td>
</tr>
<tr>
<td>P100</td>
<td>0.98</td>
</tr>
</tbody>
</table>
## NEKBOKE PERFORMANCE ANALYSIS ON P100

<table>
<thead>
<tr>
<th>Routine</th>
<th>Time (s)</th>
<th>% Solve Time</th>
<th>Model GB/s</th>
<th>Profiled GB/s</th>
<th>Model GFlops</th>
<th>Profiled GFlops</th>
<th>% Peak</th>
</tr>
</thead>
<tbody>
<tr>
<td>glsc3</td>
<td>9.23E-2</td>
<td>7.9</td>
<td>656.54</td>
<td>545.31</td>
<td>109.4</td>
<td>109.42</td>
<td>95%</td>
</tr>
<tr>
<td>add2s1</td>
<td>3.61E-2</td>
<td>3.1</td>
<td>558.20</td>
<td>512.81</td>
<td>46.5</td>
<td>46.52</td>
<td>89%</td>
</tr>
<tr>
<td>local_grad3 + wrrswt</td>
<td>3.35E-1</td>
<td>28.7</td>
<td>160.41</td>
<td>187.01</td>
<td>278.2</td>
<td>278.22</td>
<td>6%</td>
</tr>
<tr>
<td>local_grad3_t</td>
<td>2.96E-1</td>
<td>25.4</td>
<td>136.00</td>
<td>85.04</td>
<td>272.00</td>
<td>272.00</td>
<td>6%</td>
</tr>
<tr>
<td>gs_op</td>
<td>5.56E-2</td>
<td>4.8</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>add2s2</td>
<td>1.09E-1</td>
<td>9.3</td>
<td>554.37</td>
<td>512.36</td>
<td>46.5</td>
<td>46.20</td>
<td>89%</td>
</tr>
</tbody>
</table>

- Memory bandwidth intensive kernels obtain 89-95% of STREAM, better than KNL
- Compute intensive kernels obtain 6% of peak DGEMM rate, worse than KNL

- Model GB/s are within 8-10% of profiles (except glsc3, which is 20% greater)
- Model GFlops/s are within 0.03% of profiles
CONCLUSIONS AND NEXT STEPS

Conclusions:
• Nekbone performance on KNL and P100:
  • Kernels efficiencies are comparable on both for un-optimized kernels
  • Higher small matrix multiply efficiencies require architecture specific routines
  • Lowest time to solution was on KNL with optimized matrix multiply, performance is 1.3x P100
  • GPU optimized small matrix multiply could yield ~2.5x improvement in solve time on P100
    • P100 could show 1.9x better performance than KNL
• Nekbone portability:
  • “Single source” portability can be achieved with low performance on both architectures
  • OpenACC can be used performance portably for the memory bandwidth intensive routines
  • Architecture specific small matrix multiply are required for optimal performance on either architecture

Next Steps:
• Optimize small matrix multiply operations for GPU
• Merge OpenMP and OpenACC branches
• Convert OpenACC directives to OpenMP 4.5
• Look at other portability frameworks RAJA, Kokkos
QUESTIONS?
OPENACC CODE CHANGES

- Glsc3 – added OpenACC Parallel Loop pragma before loop
- Add2s2 - added OpenACC Parallel Loop pragma before loop
- Add2s1 - added OpenACC Parallel Loop pragma before loop

- Local_grad, wrwswt, local_grad_t
  - Inlined mxm calls
  - Merged 3 mxm loops
  - Inlined local_grad and local_grad_t
  - Merged local_grad and wrwswt
  - Outer loop uses ACC Parallel Loop with Collapse

- gs_gather & gs_scatter
  - Serial data structure modified to express parallelism for gather/scatters
  - OpenACC loop on over outer loop driving gather and scatter
OUTLINE

1. Nek5000 description (Scott)
2. Nekbone description (Scott)
3. Nekbone code overview (Scott)
4. Nekbone performance model (Scott)
5. KNL and P100 hardware comparison (?)
6. KNL (table or plot) for opt vs. unoptimized (Sudheer)
7. KNL comparison vs perf model and rate limit identification (Sudheer)
8. code mods for OpenACC (Scott)
9. P100 perf comparison with KNL (Ron/Sudheer)
10. P100 comparison vs perf model and rate limit identification (Ron/Sudheer)
11. Strong scaling limit discussion [Sudheer]
12. Summary of how portable [Scott]
NEKBONE IN A NUTSHELL (OPENMP)

- cg() [loop 1 -> numCGIterations]
  - solveM() [z(i) = r(i)]
  - glsc3() [inner product]
    - AllReduce()
  - add2s1() [a(i) = c*a(i)+b(i)]
  - ax()
    - ax_e()
      - local_grad3() [gradient]
        - (3x) mxm()
      - wr-ws-wt [wx(i) = f(g,ur,us,ut)]
    - local_grad3_t() [gradient]
      - (3x) mxm()
      - (2x) add2()
  - add2s2() [a(i) = a(i) + c*b(i)]
  - glsc3() [inner product]
    - AllReduce
  - add2s2() [a(i) = a(i) + c*b(i)]
  - add2s2() [a(i) = a(i) + c*b(i)]
  - glsc3() [inner product]
    - AllReduce

- Bandwidth Bound
- Compute Bound
NEKBONE COMMUNICATION

- Point to Point Communication
  - 26 send/receives per rank
    - 8 vertex values sent/received (8 Bytes per message, for 512x16 case)
    - 12 edges sent/received (128 Bytes per message, for 512x16 case)
    - 6 faces sent/received (16,384 Bytes per message, for 512x16 case)

- Collective Communication
  - Calls MPI_Allreduce 3 times per CG iteration
  - 8 Byte (1 double) reduction per call
  - 24 bytes per iteration
NEKBONE SCALING ON MIRA

Grid Points per thread: ~10k
FLOP Rate: 9% of peak
Parallel Efficiency: 99%

<table>
<thead>
<tr>
<th>Rank</th>
<th>Threads</th>
<th>TFlops</th>
</tr>
</thead>
<tbody>
<tr>
<td>512</td>
<td>64</td>
<td>9.5</td>
</tr>
<tr>
<td>1024</td>
<td>64</td>
<td>18.9</td>
</tr>
<tr>
<td>2048</td>
<td>64</td>
<td>36.9</td>
</tr>
<tr>
<td>4096</td>
<td>64</td>
<td>73.9</td>
</tr>
<tr>
<td>8192</td>
<td>64</td>
<td>150.5</td>
</tr>
<tr>
<td>16384</td>
<td>64</td>
<td>291.1</td>
</tr>
<tr>
<td>32768</td>
<td>64</td>
<td>606.9</td>
</tr>
<tr>
<td>49152</td>
<td>64</td>
<td>900.8</td>
</tr>
</tbody>
</table>

Weak Scaling Performance of Nekbone

Ideal
Actual
# TYPICAL RATIOS ON REPRESENTATIVE BG/Q RUNS

<table>
<thead>
<tr>
<th>Ratio</th>
<th>Value</th>
</tr>
</thead>
<tbody>
<tr>
<td>FLOPS/(Bytes Loaded &amp; Stored)</td>
<td>0.94</td>
</tr>
<tr>
<td>Loaded Bytes/ Stored Bytes</td>
<td>4</td>
</tr>
<tr>
<td>FLOPS/AllReduce</td>
<td>158,000,000</td>
</tr>
<tr>
<td>FLOPS/Pt2Pt Byte</td>
<td>4,744</td>
</tr>
<tr>
<td>FLOPS/MPI-Message</td>
<td>9,111,545</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th>Routine Type</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Memory Bound</td>
<td>45%</td>
</tr>
<tr>
<td>Compute Bound</td>
<td>35%</td>
</tr>
<tr>
<td>Point to Point Comm.</td>
<td>18%</td>
</tr>
<tr>
<td>Collective Comm.</td>
<td>2%</td>
</tr>
</tbody>
</table>
ADDING OPENMP TO NEKBONE

- Adding OpenMP:
  - Relatively straightforward: 90% trivial, 10% required detailed understanding
  - Basic approach: partition element across threads
    - Easy:
      - Add a single OMP parallel region at top of cg() routine
      - Modify routines (add2s2, glsc3, axi, etc) to take a range of elements as an arg
      - Modify routines to use locally declared work arrays (ax_e)
    - A bit more complex:
      - Restructure gather/scatter maps for parallel execution
      - Add synchronization and barriers around communication operations (gs_aux, pw_exec)

- Impact:
  - Little impact on compute performance
  - Little impact on memory usage
  - Some impact on communication performance, most noticeable at large scale
    - Eliminates some data copies to/from MPI buffers
    - Fewer messages sent
  - Provides opportunity to overlap communication and computation
NEKBONE ON KNL

- Nekbone is up and running on KNL
  - Simulations and estimates of performance based on KNL specs
  - Run on pre-release KNL hardware
  - Performance as expected based on compute performance model
  - Tuning use of AVX-512 instructions
    - Utilizing LIBXSMM for matrix multiplication
WHY NEKBONE?

- System designers need representative applications to study
  - HPC has unique characteristics
- In comparison to Nek5000 Nekbone is:
  - More easily configurable
    - Number of spectral elements per rank
    - Polynomial order of element
  - Quicker to run
    - Run time is adjustable over a wide range
    - Typical run time is a few seconds
  - Allows multiple cases in one run
    - A range of elements can be specified
    - A range of polynomial orders can be specified
  - More easily instrumented
  - More easily modified
    - Has ~8K lines of code vs 60k lines of code for Nek5000
    - Re-implemented using other programming models: OpenMP, OpenACC, CUDA